

# Compiling Exceptions Correctly (Extended Version)

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**Abstract.** Exceptions are an important feature of modern programming languages, but their compilation has traditionally been viewed as an advanced topic. In this article we show that the basic method of compiling exceptions using stack unwinding can be explained and verified both simply and precisely, using elementary functional programming techniques. In particular, we develop a compiler for a small language with exceptions, together with a proof of its correctness.

## 1 Introduction

Most modern programming languages support some form of programming with *exceptions*, typically based upon a primitive that abandons the current computation and *throws* an exception, together with a primitive that *catches* an exception and continues with another computation [3, 16, 11, 8]. In this article we consider the problem of compiling such exception primitives.

Exceptions have traditionally been viewed as an advanced topic in compilation, usually being discussed only briefly in courses, textbooks, and research articles, and in many cases not at all. In this article, we show that the basic method of compiling exceptions using *stack unwinding* can in fact be explained and verified both simply and precisely, using elementary functional programming techniques. In particular, we develop a compiler for a small language with exceptions, together with a proof of its correctness with respect to a formal semantics for this language. Surprisingly, this appears to be the first time that a compiler for exceptions has been proved to be correct.

In order to focus on the essence of the problem and avoid getting bogged down in other details, we adopt a particularly simple language comprising just four components, namely integer values, an addition operator, a single exceptional value called *throw*, and a *catch* operator for this value. This language does not provide features that are necessary for actual programming, but it *does* provide just what we need for our expository purposes in this article. In particular, integers and addition constitute a minimal language in which to consider computation using a stack, and *throw* and *catch* constitute a minimal extension in which such computations can involve exceptions.

Our development proceeds in three steps, starting with the language of integer values and addition, then adding *throw* and *catch* to this language, and

finally adding explicit jumps to the virtual machine. Starting with a simpler language allows us to introduce our approach to compilation and its correctness without the extra complexity of exceptions. In turn, deferring the introduction of jumps allows us to introduce our approach to the compilation of exceptions without the extra complexity of dealing with jump addresses.

All the programs in the article are written in Haskell [11], but we only use the basic concepts of recursive types, recursive functions, and inductive proofs, what might be termed the “holy trinity” of functional programming.

## 2 Arithmetic expressions

Let us begin by considering a simple language of arithmetic expressions, built up from integers using an addition operator. In Haskell, the language of such expressions can be represented by the following type:

```
data Expr = Val Int | Add Expr Expr
```

The semantics of such expressions is most naturally given denotationally [14], by defining a function that evaluates an expression to its integer value:

$$\begin{aligned} eval & :: Expr \rightarrow Int \\ eval (Val\ n) & = n \\ eval (Add\ x\ y) & = eval\ x + eval\ y \end{aligned}$$

Now let us consider compiling arithmetic expressions into code for execution using a stack machine, in which the stack is represented as a list of integers, and code comprises a list of push and add operations on the stack:

```
type Stack = [Int]
type Code = [Op]
data Op = PUSH Int | ADD
```

For ease of identification, we always use upper-case names for machine operations. Functions that compile an expression into code, and execute code using an initial stack to give a final stack, can now be defined as follows:

$$\begin{aligned} comp & :: Expr \rightarrow Code \\ comp (Val\ n) & = [PUSH\ n] \\ comp (Add\ x\ y) & = comp\ x ++ comp\ y ++ [ADD] \\ exec & :: Stack \rightarrow Code \rightarrow Stack \\ exec\ s\ [] & = s \\ exec\ s\ (PUSH\ n : ops) & = exec\ (n : s)\ ops \\ exec\ (m : n : s)\ (ADD : ops) & = exec\ (n + m : s)\ ops \end{aligned}$$

For simplicity, the function *exec* does not consider the case of *stack underflow*, which arises if the stack contains less than two integers when executing an add operation. We will return to this issue in the next section.

### 3 Compiler correctness

The correctness of our compiler for expressions with respect to our semantics can be expressed as the commutativity of the following diagram:

$$\begin{array}{ccc}
 Expr & \xrightarrow{eval} & Int \\
 \downarrow comp & & \downarrow [\cdot] \\
 Code & \xrightarrow{exec []} & Stack
 \end{array}$$

That is, compiling an expression and then executing the resulting code using an empty initial stack gives the same final stack as evaluating the expression and then converting the resulting integer into a singleton stack:

$$exec [] (comp e) = [eval e]$$

In order to prove this result, however, it is necessary to generalise from the empty initial stack to an arbitrary initial stack.

**Theorem 1 (compiler correctness).**

$$exec s (comp e) = eval e : s$$

*Proof.* By induction on  $e :: Expr$ .

Case:  $e = Val n$

$$\begin{aligned}
 & exec s (comp (Val n)) \\
 = & \quad \{ \text{definition of } comp \} \\
 & exec s [PUSH n] \\
 = & \quad \{ \text{definition of } exec \} \\
 & n : s \\
 = & \quad \{ \text{definition of } eval \} \\
 & eval (Val n) : s
 \end{aligned}$$

Case:  $e = Add x y$

$$\begin{aligned}
 & exec s (comp (Add x y)) \\
 = & \quad \{ \text{definition of } comp \} \\
 & exec s (comp x ++ comp y ++ [ADD]) \\
 = & \quad \{ \text{execution distributivity (lemma 1)} \} \\
 & exec (exec s (comp x)) (comp y ++ [ADD]) \\
 = & \quad \{ \text{induction hypothesis} \} \\
 & exec (eval x : s) (comp y ++ [ADD]) \\
 = & \quad \{ \text{execution distributivity} \} \\
 & exec (exec (eval x : s) (comp y)) [ADD] \\
 = & \quad \{ \text{induction hypothesis} \}
 \end{aligned}$$

$$\begin{aligned}
& exec (eval\ y : eval\ x : s) [ADD] \\
= & \quad \{ \text{definition of } exec \} \\
& eval\ x + eval\ y : s \\
= & \quad \{ \text{definition of } eval \} \\
& eval (Add\ x\ y) : s
\end{aligned}$$

□

Note that without first generalising the result, the second induction hypothesis step above would be invalid. The distribution lemma is as follows.

**Lemma 1 (execution distributivity).**

$$exec\ s\ (xs\ ++\ ys) = exec\ (exec\ s\ xs)\ ys$$

That is, executing two pieces of code appended together gives the same result as executing the two pieces of code separately in sequence.

*Proof.* By induction on  $xs :: Code$ .

Case:  $xs = []$

$$\begin{aligned}
& exec\ s\ ([]\ ++\ ys) \\
= & \quad \{ \text{definition of } ++ \} \\
& exec\ s\ ys \\
= & \quad \{ \text{definition of } exec \} \\
& exec\ (exec\ s\ [])\ ys
\end{aligned}$$

Case:  $xs = PUSH\ n : ops$

$$\begin{aligned}
& exec\ s\ ((PUSH\ n : ops)\ ++\ ys) \\
= & \quad \{ \text{definition of } ++ \} \\
& exec\ s\ (PUSH\ n : (ops\ ++\ ys)) \\
= & \quad \{ \text{definition of } exec \} \\
& exec\ (n : s)\ (ops\ ++\ ys) \\
= & \quad \{ \text{induction hypothesis} \} \\
& exec\ (exec\ (n : s)\ ops)\ ys \\
= & \quad \{ \text{definition of } exec \} \\
& exec\ (exec\ s\ (PUSH\ n : ops))\ ys
\end{aligned}$$

Case:  $xs = ADD : ops$

$$\begin{aligned}
& exec\ s\ ((ADD : ops)\ ++\ ys) \\
= & \quad \{ \text{definition of } ++ \} \\
& exec\ s\ (ADD : (ops\ ++\ ys)) \\
= & \quad \{ \text{assume } s \text{ of the form } m : n : s' \} \\
& exec\ (m : n : s')\ (ADD : (ops\ ++\ ys)) \\
= & \quad \{ \text{definition of } exec \} \\
& exec\ (n + m : s')\ (ops\ ++\ ys)
\end{aligned}$$

$$\begin{aligned}
&= \{ \text{induction hypothesis} \} \\
&\quad \text{exec} (\text{exec} (n + m : s') \text{ ops}) \text{ ys} \\
&= \{ \text{definition of exec} \} \\
&\quad \text{exec} (\text{exec} (m : n : s') (ADD : \text{ops})) \text{ ys}
\end{aligned}$$

□

The stack not having the assumed form in the last case of the above proof corresponds to a stack underflow error. In this case, the equation to be proved is trivially true, because the result of both sides is undefined ( $\perp$ ), provided that we assume that *exec* is strict in its stack argument ( $\text{exec } \perp \text{ ops} = \perp$ .) This extra strictness assumption could be avoided by representing and managing stack underflow explicitly, rather than doing so implicitly using  $\perp$ . In fact, however, both lemma 1 and its consequent underflow issue can be avoided altogether by further generalising our correctness theorem to also consider additional code.

**Theorem 2 (generalised compiler correctness).**

$$\text{exec } s (\text{comp } e \# \text{ ops}) = \text{exec} (\text{eval } e : s) \text{ ops}$$

That is, compiling an expression and then executing the resulting code appended together with arbitrary additional code gives the same result as pushing the value of the expression to give a new stack, which is then used to execute the additional code. Note that with  $s = \text{ops} = []$ , theorem 2 simplifies to  $\text{exec } [] (\text{comp } e) = [\text{eval } e]$ , our original statement of compiler correctness.

*Proof.* By induction on  $e :: \text{Expr}$ .

Case:  $e = \text{Val } n$

$$\begin{aligned}
&\text{exec } s (\text{comp} (\text{Val } n) \# \text{ ops}) \\
&= \{ \text{definition of comp} \} \\
&\text{exec } s ([\text{PUSH } n] \# \text{ ops}) \\
&= \{ \text{definition of exec} \} \\
&\text{exec} (n : s) \text{ ops} \\
&= \{ \text{definition of eval} \} \\
&\text{exec} (\text{eval} (\text{Val } n) : s) \text{ ops}
\end{aligned}$$

Case:  $e = \text{Add } x \ y$

$$\begin{aligned}
&\text{exec } s (\text{comp} (\text{Add } x \ y) \# \text{ ops}) \\
&= \{ \text{definition of comp} \} \\
&\text{exec } s (\text{comp } x \# \text{comp } y \# [ADD] \# \text{ ops}) \\
&= \{ \text{induction hypothesis} \} \\
&\text{exec} (\text{eval } x : s) (\text{comp } y \# [ADD] \# \text{ ops}) \\
&= \{ \text{induction hypothesis} \} \\
&\text{exec} (\text{eval } y : \text{eval } x : s) ([ADD] \# \text{ ops}) \\
&= \{ \text{definition of exec} \} \\
&\text{exec} (\text{eval } x + \text{eval } y : s) \text{ ops} \\
&= \{ \text{definition of eval} \} \\
&\text{exec} (\text{eval} (\text{Add } x \ y) : s) \text{ ops}
\end{aligned}$$



For the purposes of proofs, however, we use our non-monadic definition for *eval*. To illustrate our new semantics, here are a few simple examples:

```

eval (Add (Val 2) (Val 3)) = Just 5      - no exceptions
eval (Add Throw (Val 3))   = Nothing    - uncaught exception
eval (Catch (Val 2) (Val 3)) = Just 2    - unused handler
eval (Catch Throw (Val 3)) = Just 3     - caught exception

```

Now let us consider how the exception primitives can be compiled. First of all, we introduce three new machine operations:

```

data Op = ... | THROW | MARK Code | UNMARK

```

Informally, *THROW* throws an exception, *MARK* pushes a piece of code onto the stack, while *UNMARK* pops such code from the stack. Using these operations, our compiler for expressions can now be extended as follows:

```

comp (Throw)      = [THROW]
comp (Catch x h) = [MARK (comp h)] ++ comp x ++ [UNMARK]

```

That is, *Throw* is compiled directly to the corresponding machine operation, while *Catch x h* is compiled by first *marking the stack* with the compiled code for the handler *h*, then compiling the expression to be evaluated *x*, and finally *unmarking the stack* by removing the handler. In this way, the mark and unmark operations delimit the scope of the handler *h* to the expression *x*, in the sense that the handler is only present on the stack during execution of the expression. Note that the stack is marked with the actual compiled code for the handler, rather than some form of *pointer* to the code as would be used in a real implementation. We will return to this issue later on in the article.

Because the stack can now contain handler code as well as integer values, the type for stacks must itself be rewritten:

```

type Stack = [Item]
data Item  = VAL Int | HAN Code

```

In turn, our function that executes code is now rewritten as follows:

```

exec      :: Stack → Code → Stack
exec s [] = s
exec s (PUSH n : ops) = exec (VAL n : s) ops
exec s (ADD : ops)    = case s of
  (VAL m : VAL n : s') → exec (VAL (n + m) : s') ops
exec s (THROW : ops)   = unwind s (skip ops)
exec s (MARK ops' : ops) = exec (HAN ops' : s) ops
exec s (UNMARK : ops)  = case s of
  (x : HAN _ : s') → exec (x : s') ops

```

That is, push and add are executed as previously, except that we must now take account of the fact that values on the stack are tagged. For execution of a throw, there are a number of issues to consider. First of all, the current computation needs to be abandoned, which means removing any intermediate values that have been pushed onto the stack by this computation, as well as skipping any remaining code for this computation. And secondly, the current handler code needs to be executed, if there is any, followed by any code that remains after the abandoned computation. The function *exec* implements these ideas using an auxiliary function *unwind* that pops items from the stack until a handler is found, at which point the handler is executed followed by the remaining code, which is itself produced using a function *skip* that skips to the next unmark:

$$\begin{array}{ll}
unwind & :: \text{Stack} \rightarrow \text{Code} \rightarrow \text{Stack} \\
unwind [] \_ & = [] \\
unwind (VAL \_ : s) ops & = unwind s ops \\
unwind (HAN ops' : s) ops & = exec s (ops' \# ops) \\
skip & :: \text{Code} \rightarrow \text{Code} \\
skip [] & = [] \\
skip (UNMARK : ops) & = ops \\
skip (MARK \_ : ops) & = skip (skip ops) \\
skip (\_ : ops) & = skip ops
\end{array}$$

Note that *unwind* has the same type as *exec*, and can be viewed as an alternative mode of this function for the case when the virtual machine is in the process of handling an exception. For simplicity, *unwind* returns the empty stack in the case of an uncaught exception. For a language in which the empty stack was a valid result, a separate representation for an uncaught exception would be required. Note also the double recursion when skipping a mark, which reflects the fact that there may be nested mark/unmark pairs in the remaining code.

Returning to the remaining cases in the definition of *exec* above, a mark is executed simply by pushing the given handler code onto the stack, and dually, an unmark by popping this code from the stack. Between executing a mark and its corresponding unmark, however, the code delimited by these two operations will have pushed its result value onto the stack, and hence when the handler code is popped it will actually be the second-top item.

To illustrate our new compiler and virtual machine, their behaviour on the four example expressions from earlier in this section is shown below, in which the symbol \$\$ denotes the result of the last compilation:



$$\begin{aligned}
\text{comp } (\text{Add } (\text{Val } 2) (\text{Val } 3)) &= [\text{PUSH } 2, \text{PUSH } 3, \text{ADD}] \\
\text{exec } [] \text{ } \$\$ &= [\text{VAL } 5] \\
\text{comp } (\text{Add Throw } (\text{Val } 3)) &= [\text{THROW}, \text{PUSH } 3, \text{ADD}] \\
\text{exec } [] \text{ } \$\$ &= [] \\
\text{comp } (\text{Catch } (\text{Val } 2) (\text{Val } 3)) &= [\text{MARK } [\text{PUSH } 3], \text{PUSH } 2, \text{UNMARK}] \\
\text{exec } [] \text{ } \$\$ &= [\text{VAL } 2] \\
\text{comp } (\text{Catch Throw } (\text{Val } 3)) &= [\text{MARK } [\text{PUSH } 3], \text{THROW}, \text{UNMARK}] \\
\text{exec } [] \text{ } \$\$ &= [\text{VAL } 3]
\end{aligned}$$

## 5 Compiler correctness

Generalising from the examples in the previous section, the correctness of our new compiler is expressed by the commutativity of the following diagram:

$$\begin{array}{ccc}
\text{Expr} & \xrightarrow{\text{eval}} & \text{Maybe Int} \\
\text{comp} \downarrow & & \downarrow \text{conv} \\
\text{Code} & \xrightarrow{\text{exec } []} & \text{Stack}
\end{array}$$

That is, compiling an expression and then executing the resulting code using an empty initial stack gives the same final stack as evaluating the expression and then converting the resulting semantic value into the corresponding stack, using an auxiliary function *conv* that is defined as follows:

$$\begin{aligned}
\text{conv} &:: \text{Maybe Int} \rightarrow \text{Stack} \\
\text{conv Nothing} &= [] \\
\text{conv (Just } n) &= [\text{VAL } n]
\end{aligned}$$

As previously, however, in order to prove this result we generalise to an arbitrary initial stack and also consider additional code, and in turn rewrite the function *conv* to take account of these two extra arguments.

**Theorem 3 (compiler correctness).**

$$\text{exec } s (\text{comp } e \# \text{ops}) = \text{conv } s (\text{eval } e) \text{ops}$$

where

$$\begin{aligned}
\text{conv} &:: \text{Stack} \rightarrow \text{Maybe Int} \rightarrow \text{Code} \rightarrow \text{Stack} \\
\text{conv } s \text{ Nothing } \text{ops} &= \text{unwind } s (\text{skip } \text{ops}) \\
\text{conv } s (\text{Just } n) \text{ops} &= \text{exec } (\text{VAL } n : s) \text{ops}
\end{aligned}$$

Note that with  $s = \text{ops} = []$ , this theorem simplifies to our original statement of correctness above. The right-hand side of theorem 3 could also be written as  $\text{exec } s (\text{conv } (\text{eval } e) : \text{ops})$  using a simpler version of *conv* with type  $\text{Maybe Int} \rightarrow \text{Op}$ , but the above formulation leads to simpler proofs.

*Proof.* By induction on  $e :: Expr$ .

Case:  $e = Val\ n$

$$\begin{aligned}
& exec\ s\ (comp\ (Val\ n)\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& exec\ s\ ([PUSH\ n]\ \#\ ops) \\
= & \quad \{ \text{definition of } exec \} \\
& exec\ (VAL\ n : s)\ ops \\
= & \quad \{ \text{definition of } conv \} \\
& conv\ s\ (Just\ n)\ ops \\
= & \quad \{ \text{definition of } eval \} \\
& conv\ s\ (eval\ (Val\ n))\ ops
\end{aligned}$$

Case:  $e = Throw$

$$\begin{aligned}
& exec\ s\ (comp\ Throw\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& exec\ s\ ([THROW]\ \#\ ops) \\
= & \quad \{ \text{definition of } exec \} \\
& unwind\ s\ (skip\ ops) \\
= & \quad \{ \text{definition of } conv \} \\
& conv\ s\ Nothing\ ops \\
= & \quad \{ \text{definition of } eval \} \\
& conv\ s\ (eval\ Throw)\ ops
\end{aligned}$$

Case:  $e = Add\ x\ y$

$$\begin{aligned}
& exec\ s\ (comp\ (Add\ x\ y)\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& exec\ s\ (comp\ x\ \#\ comp\ y\ \#\ [ADD]\ \#\ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& conv\ s\ (eval\ x)\ (comp\ y\ \#\ [ADD]\ \#\ ops) \\
= & \quad \{ \text{definition of } conv \} \\
& \mathbf{case\ } eval\ x\ \mathbf{of} \\
& \quad Nothing \rightarrow unwind\ s\ (skip\ (comp\ y\ \#\ [ADD]\ \#\ ops)) \\
& \quad Just\ n \rightarrow exec\ (VAL\ n : s)\ (comp\ y\ \#\ [ADD]\ \#\ ops)
\end{aligned}$$

The two possible results from this expression are simplified below.

1:

$$\begin{aligned}
& unwind\ s\ (skip\ (comp\ y\ \#\ [ADD]\ \#\ ops)) \\
= & \quad \{ \text{skipping compiled code (lemma 2)} \} \\
& unwind\ s\ (skip\ ([ADD]\ \#\ ops)) \\
= & \quad \{ \text{definition of } skip \} \\
& unwind\ s\ (skip\ ops)
\end{aligned}$$

2:

$$\begin{aligned}
& \text{exec } (VAL \ n : s) \ (comp \ y \ ++ \ [ADD] \ ++ \ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{conv } (VAL \ n : s) \ (eval \ y) \ ([ADD] \ ++ \ ops) \\
= & \quad \{ \text{definition of conv} \} \\
& \mathbf{case \ eval \ y \ of} \\
& \quad \text{Nothing} \ \rightarrow \ unwind \ (VAL \ n : s) \ (skip \ ([ADD] \ ++ \ ops)) \\
& \quad \text{Just } m \ \rightarrow \ exec \ (VAL \ m : VAL \ n : s) \ ([ADD] \ ++ \ ops) \\
= & \quad \{ \text{definition of unwind, skip and exec} \} \\
& \mathbf{case \ eval \ y \ of} \\
& \quad \text{Nothing} \ \rightarrow \ unwind \ s \ (skip \ ops) \\
& \quad \text{Just } m \ \rightarrow \ exec \ (VAL \ (n + m) : s) \ ops
\end{aligned}$$

We now continue the calculation using the two simplified results.

$$\begin{aligned}
& \mathbf{case \ eval \ x \ of} \\
& \quad \text{Nothing} \ \rightarrow \ unwind \ s \ (skip \ ops) \\
& \quad \text{Just } n \ \rightarrow \ \mathbf{case \ eval \ y \ of} \\
& \quad \quad \text{Nothing} \ \rightarrow \ unwind \ s \ (skip \ ops) \\
& \quad \quad \text{Just } m \ \rightarrow \ exec \ (VAL \ (n + m) : s) \ ops \\
= & \quad \{ \text{definition of conv} \} \\
& \mathbf{case \ eval \ x \ of} \\
& \quad \text{Nothing} \ \rightarrow \ conv \ s \ \text{Nothing} \ ops \\
& \quad \text{Just } n \ \rightarrow \ \mathbf{case \ eval \ y \ of} \\
& \quad \quad \text{Nothing} \ \rightarrow \ conv \ s \ \text{Nothing} \ ops \\
& \quad \quad \text{Just } m \ \rightarrow \ conv \ s \ (\text{Just } (n + m)) \ ops \\
= & \quad \{ \text{distribution over case} \} \\
& \text{conv } s \ (\mathbf{case \ eval \ x \ of} \\
& \quad \text{Nothing} \ \rightarrow \ \text{Nothing} \\
& \quad \text{Just } n \ \rightarrow \ \mathbf{case \ eval \ y \ of} \\
& \quad \quad \text{Nothing} \ \rightarrow \ \text{Nothing} \\
& \quad \quad \text{Just } m \ \rightarrow \ \text{Just } (n + m)) \ ops \\
= & \quad \{ \text{definition of eval} \} \\
& \text{conv } s \ (eval \ (Add \ x \ y)) \ ops
\end{aligned}$$

Case:  $e = Catch \ x \ h$

$$\begin{aligned}
& \text{exec } s \ (comp \ (Catch \ x \ h) \ ++ \ ops) \\
= & \quad \{ \text{definition of comp} \} \\
& \text{exec } s \ ([MARK \ (comp \ h)] \ ++ \ comp \ x \ ++ \ [UNMARK] \ ++ \ ops) \\
= & \quad \{ \text{definition of exec} \} \\
& \text{exec } (HAN \ (comp \ h) : s) \ (comp \ x \ ++ \ [UNMARK] \ ++ \ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{conv } (HAN \ (comp \ h) : s) \ (eval \ x) \ ([UNMARK] \ ++ \ ops) \\
= & \quad \{ \text{definition of conv} \} \\
& \mathbf{case \ eval \ x \ of} \\
& \quad \text{Nothing} \ \rightarrow \ unwind \ (HAN \ (comp \ h) : s) \ (skip \ ([UNMARK] \ ++ \ ops)) \\
& \quad \text{Just } n \ \rightarrow \ exec \ (VAL \ n : HAN \ (comp \ h) : s) \ ([UNMARK] \ ++ \ ops)
\end{aligned}$$

$$\begin{aligned}
&= \{ \text{definition of } unwind, skip \text{ and } exec \} \\
&\quad \mathbf{case\ } eval\ x\ \mathbf{of} \\
&\quad \quad Nothing \rightarrow exec\ s\ (comp\ h\ \# ops) \\
&\quad \quad Just\ n \rightarrow exec\ (VAL\ n : s)\ ops \\
&= \{ \text{induction hypothesis} \} \\
&\quad \mathbf{case\ } eval\ x\ \mathbf{of} \\
&\quad \quad Nothing \rightarrow conv\ s\ (eval\ h)\ ops \\
&\quad \quad Just\ n \rightarrow exec\ (VAL\ n : s)\ ops \\
&= \{ \text{definition of } conv \} \\
&\quad \mathbf{case\ } eval\ x\ \mathbf{of} \\
&\quad \quad Nothing \rightarrow conv\ s\ (eval\ h)\ ops \\
&\quad \quad Just\ n \rightarrow conv\ s\ (Just\ n)\ ops \\
&= \{ \text{distribution over } \mathbf{case} \} \\
&\quad conv\ s\ (\mathbf{case\ } eval\ x\ \mathbf{of} \\
&\quad \quad Nothing \rightarrow eval\ h \\
&\quad \quad Just\ n \rightarrow Just\ n)\ ops \\
&= \{ \text{definition of } eval \} \\
&\quad conv\ s\ (eval\ (Catch\ x\ h))\ ops
\end{aligned}$$

□

The two distribution over **case** steps in the above proof rely on the fact that *conv* is strict in its semantic value argument ( $conv\ s\ \perp\ ops = \perp$ ), which is indeed the case because *conv* is defined by pattern matching on this argument. The skipping lemma used in the above proof is as follows.

**Lemma 2 (skipping compiled code).**

$$skip\ (comp\ e\ \# ops) = skip\ ops$$

That is, skipping to the next unmark in compiled code followed by arbitrary additional code gives the same result as simply skipping the additional code. Intuitively, this is the case because the compiler ensures that all unmarks in compiled code are matched by preceding marks.

*Proof.* By induction on  $e :: Expr$ .

Case:  $e = Val\ n$

$$\begin{aligned}
&skip\ (comp\ (Val\ n)\ \# ops) \\
&= \{ \text{definition of } comp \} \\
&skip\ ([PUSH\ n]\ \# ops) \\
&= \{ \text{definition of } skip \} \\
&skip\ ops
\end{aligned}$$

Case:  $e = Throw$

$$\begin{aligned}
&skip\ (comp\ Throw\ \# ops) \\
&= \{ \text{definition of } comp \}
\end{aligned}$$

$$\begin{aligned}
& \text{skip } ([\text{THROW}] \# ops) \\
= & \quad \{ \text{definition of skip} \} \\
& \text{skip } ops
\end{aligned}$$

Case:  $e = \text{Add } x \ y$

$$\begin{aligned}
& \text{skip } (\text{comp } (\text{Add } x \ y) \# ops) \\
= & \quad \{ \text{definition of comp} \} \\
& \text{skip } (\text{comp } x \# \text{comp } y \# [\text{ADD}] \# ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{skip } (\text{comp } y \# [\text{ADD}] \# ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{skip } ([\text{ADD}] \# ops) \\
= & \quad \{ \text{definition of skip} \} \\
& \text{skip } ops
\end{aligned}$$

Case:  $e = \text{Catch } x \ h$

$$\begin{aligned}
& \text{skip } (\text{comp } (\text{Catch } x \ h) \# ops) \\
= & \quad \{ \text{definition of comp} \} \\
& \text{skip } ([\text{MARK } (\text{comp } h)] \# \text{comp } x \# [\text{UNMARK}] \# ops) \\
= & \quad \{ \text{definition of skip} \} \\
& \text{skip } (\text{skip } (\text{comp } x \# [\text{UNMARK}] \# ops)) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{skip } (\text{skip } ([\text{UNMARK}] \# ops)) \\
= & \quad \{ \text{definition of skip} \} \\
& \text{skip } ops
\end{aligned}$$

□

## 6 Adding jumps

Now let us extend our virtual machine with primitives that allow exceptions to be compiled using explicit jumps, rather than by pushing handler code onto the stack. First of all, we introduce three new machine operations:

**data**  $Op = \dots \mid \text{MARK } Addr \mid \text{LABEL } Addr \mid \text{JUMP } Addr$

Informally, *MARK* pushes the address of a piece of code onto the stack (replacing our previous mark operator that pushed code itself), *LABEL* declares an address, and *JUMP* transfers control to an address. Addresses themselves are represented simply as integers, and we ensure that each address is declared at most once by generating addresses in sequential order using a function *fresh*:

$$\begin{aligned}
\mathbf{type} \quad Addr &= Int \\
fresh &:: Addr \rightarrow Addr \\
fresh \ a &= a + 1
\end{aligned}$$

Our compiler for expressions is now extended to take the next fresh address as an additional argument, and is rewritten in terms of another function *compile* that also returns the next fresh address as an additional result:

$$\begin{aligned}
 \text{comp} & \quad :: \text{Addr} \rightarrow \text{Expr} \rightarrow \text{Code} \\
 \text{comp } a \ e & = \text{fst} (\text{compile } a \ e) \\
 \\
 \text{compile} & \quad :: \text{Addr} \rightarrow \text{Expr} \rightarrow (\text{Code}, \text{Addr}) \\
 \text{compile } a \ (\text{Val } n) & = ([\text{PUSH } n], a) \\
 \text{compile } a \ (\text{Add } x \ y) & = (xs ++ ys ++ [\text{ADD}], c) \\
 & \quad \textbf{where} \\
 & \quad \quad (xs, b) = \text{compile } a \ x \\
 & \quad \quad (ys, c) = \text{compile } a \ y \\
 \text{compile } a \ (\text{Throw}) & = ([\text{THROW}], a) \\
 \text{compile } a \ (\text{Catch } x \ h) & = ([\text{MARK } a] ++ xs ++ [\text{UNMARK}, \text{JUMP } b, \\
 & \quad \quad \text{LABEL } a] ++ hs ++ [\text{LABEL } b], e) \\
 & \quad \textbf{where} \\
 & \quad \quad b = \text{fresh } a \\
 & \quad \quad c = \text{fresh } b \\
 & \quad \quad (xs, d) = \text{compile } a \ x \\
 & \quad \quad (hs, e) = \text{compile } a \ h
 \end{aligned}$$

Note that integer values, addition, and throw are compiled as previously, except that the next fresh address is threaded through, while *Catch x h* is now compiled to the following sequence, in which *a*: abbreviates *LABEL a*:

$$\begin{aligned}
 & \text{MARK } a \\
 & \text{compiled code for } x \\
 & \text{UNMARK} \\
 & \text{JUMP } b \\
 & a: \text{compiled code for } h \\
 & b: \text{rest of the code}
 \end{aligned}$$

That is, *Catch x h* is now compiled by first marking the stack with the address of the compiled code for the handler *h*, compiling the expression to be evaluated *x*, then unmarking the stack by removing the address of the handler, and finally jumping over the handler code to the rest of the code.

By exploiting the fact that the type for *compile* can be expressed using a *state monad* [17], the above definition can also be expressed more abstractly and concisely using monadic syntax. As with the function *eval*, however, for the purposes of proofs we use our non-monadic definition for *compile*.

Because the stack can now contain handler addresses rather than handler code, the type for stack items must be rewritten:

$$\text{data Item} = \text{VAL Int} \mid \text{HAN Addr}$$

In turn, our function that executes code requires four modifications:

$$\begin{aligned}
exec\ s\ (THROW\ :\ ops) &= unwind\ s\ ops \\
exec\ s\ (MARK\ a\ :\ ops) &= exec\ (HAN\ a\ :\ s)\ ops \\
exec\ s\ (LABEL\ \_:\ ops) &= exec\ s\ ops \\
exec\ s\ (JUMP\ a\ :\ ops) &= exec\ s\ (jump\ a\ ops)
\end{aligned}$$

For execution of a throw, the use of explicit jumps means that the function *skip* is no longer required, and there are now only two issues to consider. First of all, the current computation needs to be abandoned, by removing any intermediate values that have been pushed onto the stack by this computation. And secondly, the current handler needs to be executed, if there is any. Implementing these ideas requires modifying one line in the definition of *unwind*:

$$unwind\ (HAN\ a\ :\ s)\ ops = exec\ s\ (jump\ a\ ops)$$

That is, once the address of a handler is found, the handler is executed using a function *jump* that transfers control to a given address:

$$\begin{aligned}
jump &:: Addr \rightarrow Code \rightarrow Code \\
jump\ \_ &= [] \\
jump\ a\ (LABEL\ b\ :\ ops) &= \mathbf{if}\ a == b\ \mathbf{then}\ ops\ \mathbf{else}\ jump\ a\ ops \\
jump\ a\ (\_:\ ops) &= jump\ a\ ops
\end{aligned}$$

Note that our language only requires forward jumps. If backward jumps were also possible, a slightly generalised virtual machine would be required.

Returning to the remaining modified cases in the definition of *exec* above, a mark is executed simply by pushing the given address onto the stack, a label is executed by skipping the label, and a jump is executed by transferring control to the given address using the function *jump* defined above.

The behaviour of our new compiler and virtual machine on the four example expressions from earlier in the article is shown below:

$$\begin{aligned}
comp\ 0\ (Add\ (Val\ 2)\ (Val\ 3)) &= [PUSH\ 2,\ PUSH\ 3,\ ADD] \\
exec\ []\ \$\$ &= [VAL\ 5] \\
\\
comp\ 0\ (Add\ Throw\ (Val\ 3)) &= [THROW,\ PUSH\ 3,\ ADD] \\
exec\ []\ \$\$ &= [] \\
\\
comp\ 0\ (Catch\ (Val\ 2)\ (Val\ 3)) &= [MARK\ 0,\ PUSH\ 2,\ UNMARK,\ JUMP\ 1, \\
&\quad LABEL\ 0,\ PUSH\ 3,\ LABEL\ 1] \\
exec\ []\ \$\$ &= [VAL\ 2] \\
\\
comp\ 0\ (Catch\ Throw\ (Val\ 3)) &= [MARK\ 0,\ THROW,\ UNMARK,\ JUMP\ 1, \\
&\quad LABEL\ 0,\ PUSH\ 3,\ LABEL\ 1] \\
exec\ []\ \$\$ &= [VAL\ 3]
\end{aligned}$$

Note that our compiler now once again produces “flat” code, in contrast to our previous version, which produced tree-structured code.

## 7 Compiler correctness

The correctness of our new compiler is expressed by the same diagram as the previous version, except that new compiler takes the next fresh address as an additional argument, for which we supply zero as the initial value:

$$\begin{array}{ccc}
 Expr & \xrightarrow{eval} & Maybe\ Int \\
 \downarrow \text{comp } 0 & & \downarrow \text{conv} \\
 Code & \xrightarrow{exec []} & Stack
 \end{array}$$

For the purposes of proofs we once again generalise this result to an arbitrary initial stack and additional code, and extend the function *conv* accordingly. In turn, we also generalise to an arbitrary initial address that is fresh with respect to the initial stack, using a predicate *isFresh* that decides if a given address is greater than every address that occurs in a stack.

**Theorem 4 (compiler correctness).**

*If  $isFresh\ a\ s$  then  $exec\ s\ (comp\ a\ e\ \# ops) = conv\ s\ (eval\ e)\ ops$*

where

$$\begin{array}{ll}
 isFresh & ::\ Addr \rightarrow Stack \rightarrow Bool \\
 isFresh\ \_ [] & =\ True \\
 isFresh\ a\ (VAL\ \_ : s) & =\ isFresh\ a\ s \\
 isFresh\ a\ (HAN\ b : s) & =\ a > b \wedge isFresh\ a\ s \\
 conv & ::\ Stack \rightarrow Maybe\ Int \rightarrow Code \rightarrow Stack \\
 conv\ s\ Nothing\ ops & =\ unwind\ s\ ops \\
 conv\ s\ (Just\ n)\ ops & =\ exec\ (VAL\ n : s)\ ops
 \end{array}$$

*Proof.* By induction on  $e :: Expr$  in a similar manner to theorem 3, except that five lemmas concerning fresh addresses are required.

Case:  $e = Val\ n$

$$\begin{aligned}
 & exec\ s\ (comp\ a\ (Val\ n)\ \# ops) \\
 = & \quad \{ \text{definition of } comp \} \\
 & exec\ s\ ([PUSH\ n]\ \# ops) \\
 = & \quad \{ \text{definition of } exec \} \\
 & exec\ (VAL\ n : s)\ ops \\
 = & \quad \{ \text{definition of } conv \} \\
 & conv\ s\ (Just\ n)\ ops \\
 = & \quad \{ \text{definition of } eval \} \\
 & conv\ s\ (eval\ (Val\ n))\ ops
 \end{aligned}$$



Case:  $e = \text{Throw}$

$$\begin{aligned} & \text{exec } s \text{ (comp } a \text{ Throw } \# \text{ ops)} \\ = & \quad \{ \text{definition of comp } \} \\ & \text{exec } s \text{ ([THROW] } \# \text{ ops)} \\ = & \quad \{ \text{definition of exec } \} \\ & \text{unwind } s \text{ ops} \\ = & \quad \{ \text{definition of conv } \} \\ & \text{conv } s \text{ Nothing ops} \\ = & \quad \{ \text{definition of eval } \} \\ & \text{conv } s \text{ (eval Throw) ops} \end{aligned}$$

Case:  $e = \text{Add } x \ y$

Let

$$\begin{aligned} (\_ , b) &= \text{compile } a \ x \\ (\_ , c) &= \text{compile } b \ y \end{aligned}$$

in

$$\begin{aligned} & \text{exec } s \text{ (comp } a \text{ (Add } x \ y) \# \text{ ops)} \\ = & \quad \{ \text{definition of comp } \} \\ & \text{exec } s \text{ (comp } a \ x \# \text{ comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \\ = & \quad \{ \text{induction hypothesis } \} \\ & \text{conv } s \text{ (eval } x) \text{ (comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \\ = & \quad \{ \text{definition of conv } \} \\ & \text{case eval } x \text{ of} \\ & \quad \text{Nothing} \rightarrow \text{unwind } s \text{ (comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \\ & \quad \text{Just } n \rightarrow \text{exec } (\text{VAL } n : s) \text{ (comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \end{aligned}$$

The two possible results from this expression are simplified below.

1:

$$\begin{aligned} & \text{unwind } s \text{ (comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \\ = & \quad \{ \text{unwinding compiled code (lemma 4) } \} \\ & \text{unwind } s \text{ ([ADD] } \# \text{ ops)} \\ = & \quad \{ \text{unwinding operators (lemma 3) } \} \\ & \text{unwind } s \text{ ops} \end{aligned}$$

2:

$$\begin{aligned} & \text{exec } (\text{VAL } n : s) \text{ (comp } b \ y \# \text{ [ADD] } \# \text{ ops)} \\ = & \quad \{ \text{induction hypothesis } \} \\ & \text{conv } (\text{VAL } n : s) \text{ (eval } y) \text{ ([ADD] } \# \text{ ops)} \\ = & \quad \{ \text{definition of conv } \} \\ & \text{case eval } y \text{ of} \\ & \quad \text{Nothing} \rightarrow \text{unwind } (\text{VAL } n : s) \text{ ([ADD] } \# \text{ ops)} \end{aligned}$$

$$\begin{aligned}
& \text{Just } m \rightarrow \text{exec } (\text{VAL } m : \text{VAL } n : s) ([\text{ADD}] \# \text{ops}) \\
= & \{ \text{unwinding operators, definition of } \textit{unwind} \text{ and } \textit{exec} \} \\
& \mathbf{case \textit{eval } y \textit{ of}} \\
& \quad \text{Nothing} \rightarrow \textit{unwind } s \textit{ ops} \\
& \quad \text{Just } m \rightarrow \text{exec } (\text{VAL } (n + m) : s) \textit{ ops}
\end{aligned}$$

We now continue the calculation using the two simplified results.

$$\begin{aligned}
& \mathbf{case \textit{eval } x \textit{ of}} \\
& \quad \text{Nothing} \rightarrow \textit{unwind } s \textit{ ops} \\
& \quad \text{Just } n \rightarrow \mathbf{case \textit{eval } y \textit{ of}} \\
& \quad \quad \text{Nothing} \rightarrow \textit{unwind } s \textit{ ops} \\
& \quad \quad \text{Just } m \rightarrow \text{exec } (\text{VAL } (n + m) : s) \textit{ ops} \\
= & \{ \text{definition of } \textit{conv} \} \\
& \mathbf{case \textit{eval } x \textit{ of}} \\
& \quad \text{Nothing} \rightarrow \textit{conv } s \textit{ Nothing ops} \\
& \quad \text{Just } n \rightarrow \mathbf{case \textit{eval } y \textit{ of}} \\
& \quad \quad \text{Nothing} \rightarrow \textit{conv } s \textit{ Nothing ops} \\
& \quad \quad \text{Just } m \rightarrow \textit{conv } s (\text{Just } (n + m)) \textit{ ops} \\
= & \{ \text{distribution over } \mathbf{case} \} \\
& \textit{conv } s (\mathbf{case \textit{eval } x \textit{ of}} \\
& \quad \text{Nothing} \rightarrow \textit{Nothing} \\
& \quad \text{Just } n \rightarrow \mathbf{case \textit{eval } y \textit{ of}} \\
& \quad \quad \text{Nothing} \rightarrow \textit{Nothing} \\
& \quad \quad \text{Just } m \rightarrow \text{Just } (n + m)) \textit{ ops} \\
= & \{ \text{definition of } \textit{eval} \} \\
& \textit{conv } s (\textit{eval } (\textit{Add } x \textit{ y})) \textit{ ops}
\end{aligned}$$

Note that a number of steps in the above proof rely on freshness conditions. The first induction hypothesis step requires that  $\textit{isFresh } a \ s$ , which is true by assumption. The unwinding compiled code (lemma 4) step requires that  $\textit{isFresh } b \ s$ , which is verified by the following calculation:

$$\begin{aligned}
& \textit{isFresh } b \ s \\
\Leftarrow & \{ \textit{isFresh} \text{ is monotonic (lemma 5)} \} \\
& a \leq b \ \wedge \ \textit{isFresh } a \ s \\
\Leftrightarrow & \{ \text{definition of } b \} \\
& a \leq \textit{snd } (\textit{compile } a \ x) \ \wedge \ \textit{isFresh } a \ s \\
\Leftarrow & \{ \textit{compile} \text{ is non-decreasing (lemma 6), assumption} \} \\
& \textit{True}
\end{aligned}$$

Finally, the second induction hypothesis step requires that  $\textit{isFresh } b \ (\textit{Val } n : s)$ , which is trivially true by the definition of  $\textit{isFresh}$  and the previous condition.

Case:  $e = \textit{Catch } x \ h$

Let

$b$  = fresh  $a$   
 $c$  = fresh  $b$   
 $(\_, d)$  = compile  $c$   $x$   
 $(\_, e)$  = compile  $d$   $h$

in

$exec\ s\ (comp\ a\ (Catch\ x\ h)\ \#\ ops)$   
 $=$  { definition of  $comp$  }  
 $exec\ s\ ([MARK\ a]\ \#\ comp\ c\ x\ \#\ [UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $=$  { definition of  $exec$  }  
 $exec\ (HAN\ a : s)\ (comp\ c\ x\ \#\ (UNMARK,\ JUMP\ b,\ LABEL\ a)\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $=$  { induction hypothesis }  
 $conv\ (HAN\ a : s)\ (eval\ x)\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $=$  { definition of  $conv$  }  
**case**  $eval\ x$  **of**  
 $Nothing \rightarrow unwind\ (HAN\ a : s)\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $Just\ n \rightarrow exec\ (VAL\ n : HAN\ a : s)\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$

The two possible results from this expression are simplified below.

1:

$unwind\ (HAN\ a : s)\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $=$  { definition of  $unwind$  }  
 $exec\ s\ (jump\ a\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops))$   
 $=$  { definition of  $jump$  }  
 $exec\ s\ (comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops)$   
 $=$  { induction hypothesis }  
 $conv\ s\ (eval\ h)\ ([LABEL\ b]\ \#\ ops)$   
 $=$  { definition of  $conv$  }  
**case**  $eval\ h$  **of**  
 $Nothing \rightarrow unwind\ s\ ([LABEL\ b]\ \#\ ops)$   
 $Just\ m \rightarrow exec\ (VAL\ m : s)\ ([LABEL\ b]\ \#\ ops)$   
 $=$  { unwinding operators (lemma 3), definition of  $exec$  }  
**case**  $eval\ h$  **of**  
 $Nothing \rightarrow unwind\ s\ ops$   
 $Just\ m \rightarrow exec\ (VAL\ m : s)\ ops$

2:

$$\begin{aligned}
& \text{exec } (VAL\ n : HAN\ a : s) ([UNMARK, JUMP\ b, \\
& \quad LABEL\ a] \# \text{comp } d\ h \# [LABEL\ b] \# ops) \\
= & \quad \{ \text{definition of } exec \} \\
& \text{exec } (VAL\ n : s) (\text{jump } b ([LABEL\ a] \# \text{comp } d\ h \# [LABEL\ b] \# ops)) \\
= & \quad \{ \text{definition of } jump, a \neq b \} \\
& \text{exec } (VAL\ n : s) (\text{jump } b (\text{comp } d\ h \# [LABEL\ b] \# ops)) \\
= & \quad \{ \text{jumping compiled code (lemma 7)} \} \\
& \text{exec } (VAL\ n : s) (\text{jump } b ([LABEL\ b] \# ops)) \\
= & \quad \{ \text{definition of } jump \} \\
& \text{exec } (VAL\ n : s) ops
\end{aligned}$$

We now continue the calculation using the two simplified results.

$$\begin{aligned}
& \text{case } eval\ x\ \text{of} \\
& \quad Nothing \rightarrow \text{case } eval\ h\ \text{of} \\
& \quad \quad Nothing \rightarrow unwind\ s\ ops \\
& \quad \quad Just\ m \rightarrow exec\ (VAL\ m : s)\ ops \\
& \quad Just\ n \rightarrow exec\ (VAL\ n : s)\ ops \\
= & \quad \{ \text{definition of } conv \} \\
& \text{case } eval\ x\ \text{of} \\
& \quad Nothing \rightarrow \text{case } eval\ h\ \text{of} \\
& \quad \quad Nothing \rightarrow conv\ s\ Nothing\ ops \\
& \quad \quad Just\ m \rightarrow conv\ s\ (Just\ m)\ ops \\
& \quad Just\ n \rightarrow conv\ s\ (Just\ n)\ ops \\
= & \quad \{ \text{distribution over } case \} \\
& conv\ s\ (\text{case } eval\ x\ \text{of} \\
& \quad Nothing \rightarrow \text{case } eval\ h\ \text{of} \\
& \quad \quad Nothing \rightarrow Nothing \\
& \quad \quad Just\ m \rightarrow Just\ m \\
& \quad Just\ n \rightarrow Just\ n)\ ops \\
= & \quad \{ \text{definition of } eval \} \\
& conv\ s\ (eval\ (Catch\ x\ h))\ ops
\end{aligned}$$

Again, a number of steps in the above proof rely on freshness conditions. The first induction hypothesis step requires that  $isFresh\ c\ (HAN\ a : s)$ :

$$\begin{aligned}
& isFresh\ c\ (HAN\ a : s) \\
\Leftrightarrow & \quad \{ \text{definition of } isFresh \} \\
& c > a \ \wedge \ isFresh\ c\ s \\
\Leftrightarrow & \quad \{ \text{definition of } c \} \\
& a + 2 > a \ \wedge \ isFresh\ (a + 2)\ s \\
\Leftrightarrow & \quad \{ \text{simplification} \} \\
& isFresh\ (a + 2)\ s \\
\Leftarrow & \quad \{ isFresh\ \text{is monotonic (lemma 5)} \} \\
& isFresh\ a\ s \\
\Leftrightarrow & \quad \{ \text{assumption} \} \\
& True
\end{aligned}$$

The jumping compiled code (lemma 7) step requires that  $b < d$ :

$$\begin{aligned}
& b \\
< & \quad \{ \text{definition of } c \} \\
& c \\
\leq & \quad \{ \text{compile is non-decreasing (lemma 6)} \} \\
& \text{snd } (\text{compile } c \ x) \\
= & \quad \{ \text{definition of } d \} \\
& d
\end{aligned}$$

The unwinding operators (lemma 3) step requires that  $\text{isFresh } b \ s$ :

$$\begin{aligned}
& \text{isFresh } b \ s \\
\Leftarrow & \quad \{ \text{isFresh is monotonic} \} \\
& a \leq b \ \wedge \ \text{isFresh } a \ s \\
\Leftrightarrow & \quad \{ \text{definition of } b, \text{ assumption} \} \\
& \text{True}
\end{aligned}$$

And finally, the second induction hypothesis step requires that  $\text{isFresh } d \ s$ :

$$\begin{aligned}
& \text{isFresh } d \ s \\
\Leftarrow & \quad \{ \text{isFresh is monotonic} \} \\
& b \leq d \ \wedge \ \text{isFresh } b \ s \\
\Leftrightarrow & \quad \{ \text{previous two conditions} \} \\
& \text{True}
\end{aligned}$$

□

## 8 Freshness properties

This section presents the five properties concerning fresh addresses used in the previous section to prove the correctness of our final compiler.

**Lemma 3 (unwinding operators).**

*If  $op = \text{LABEL } a \Rightarrow \text{isFresh } a \ s$  then*

$$\text{unwind } s \ (op : ops) = \text{unwind } s \ ops$$

That is, when unwinding the stack the first operator in the code can be discarded, provided that it is not an address that may occur in the stack.

*Proof.* By induction on  $s :: \text{Stack}$ .

$$\begin{aligned}
\text{Case: } s &= [] \\
& \text{unwind } [] \ (op : ops) \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& [] \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& \text{unwind } [] \ ops
\end{aligned}$$

Case:  $s = \text{VAL } n : s'$

$$\begin{aligned}
& \text{unwind } (\text{VAL } n : s') (op : ops) \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& \text{unwind } s' (op : ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{unwind } s' ops \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& \text{unwind } (\text{VAL } n : s') ops
\end{aligned}$$

The induction hypothesis step in the above proof requires that  $op = \text{LABEL } a \Rightarrow \text{isFresh } a s'$ , which is verified by the following calculation:

$$\begin{aligned}
& op = \text{LABEL } a \\
\Rightarrow & \quad \{ \text{assumption} \} \\
& \text{isFresh } a (\text{VAL } n : s') \\
\Leftrightarrow & \quad \{ \text{definition of } \text{isFresh} \} \\
& \text{isFresh } a s'
\end{aligned}$$

Case:  $s = \text{HAN } a : s'$

$$\begin{aligned}
& \text{unwind } (\text{HAN } a : s') (op : ops) \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& \text{exec } s' (\text{jump } a (op : ops)) \\
= & \quad \{ \text{definition of } \text{jump}, op \neq \text{LABEL } a \} \\
& \text{exec } s' (\text{jump } a ops) \\
= & \quad \{ \text{definition of } \text{unwind} \} \\
& \text{unwind } (\text{HAN } a : s') ops
\end{aligned}$$

The condition  $op \neq \text{LABEL } a$  follows from the assumption of the lemma:

$$\begin{aligned}
& op = \text{LABEL } b \Rightarrow \text{isFresh } b (\text{HAN } a : s') \\
\Leftrightarrow & \quad \{ \text{definition of } \text{isFresh} \} \\
& op = \text{LABEL } b \Rightarrow (b > a \wedge \text{isFresh } b s') \\
\Rightarrow & \quad \{ \text{logic} \} \\
& op = \text{LABEL } b \Rightarrow b > a \\
\Leftrightarrow & \quad \{ \text{contraposition} \} \\
& b \leq a \Rightarrow op \neq \text{LABEL } b \\
\Rightarrow & \quad \{ \text{taking } a = b \} \\
& op \neq \text{LABEL } a
\end{aligned}$$

□

**Lemma 4 (unwinding compiled code).**

*If  $\text{isFresh } a s$  then  $\text{unwind } s (\text{comp } a e \# ops) = \text{unwind } s ops$*

That is, unwinding the stack on compiled code followed by arbitrary additional code gives the same result as simply unwinding the stack on the additional code, provided that the initial address for the compiler is fresh for the stack.

*Proof.* By induction on  $e :: Expr$ .

Case:  $e = Val\ n$

$$\begin{aligned}
& unwind\ s\ (comp\ a\ (Val\ n)\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& unwind\ s\ ([PUSH\ n]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators (lemma 3)} \} \\
& unwind\ s\ ops
\end{aligned}$$

Case:  $e = Throw$

$$\begin{aligned}
& unwind\ s\ (comp\ a\ Throw\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& unwind\ s\ ([THROW]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators} \} \\
& unwind\ s\ ops
\end{aligned}$$

Case:  $e = Add\ x\ y$

Let

$$\begin{aligned}
(\_ , b) &= compile\ a\ x \\
(\_ , c) &= compile\ b\ y
\end{aligned}$$

in

$$\begin{aligned}
& unwind\ s\ (comp\ a\ (Add\ x\ y)\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& unwind\ s\ (comp\ a\ x\ \#\ comp\ b\ y\ \#\ [ADD]\ \#\ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& unwind\ s\ (comp\ b\ y\ \#\ [ADD]\ \#\ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& unwind\ s\ ([ADD]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators} \} \\
& unwind\ s\ ops
\end{aligned}$$

The two induction hypothesis steps in the above proof require that *isFresh a s* and *isFresh b s* respectively, the first of which is true by assumption, and the second of which was verified in the previous section.

Case:  $e = Catch\ x\ h$

Let

$$\begin{aligned}
b &= fresh\ a \\
c &= fresh\ b \\
(\_ , d) &= compile\ c\ x \\
(\_ , e) &= compile\ d\ h
\end{aligned}$$

in

$$\begin{aligned}
& unwind\ s\ (comp\ a\ (Catch\ x\ h)\ \#\ ops) \\
= & \quad \{ \text{definition of } comp \} \\
& unwind\ s\ ([MARK\ a]\ \#\ comp\ c\ x\ \#\ [UNMARK,\ JUMP\ b,\ \\
& \quad LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators} \} \\
& unwind\ s\ (comp\ c\ x\ \#\ [UNMARK,\ JUMP\ b,\ \\
& \quad LABEL\ a]\ \#\ comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& unwind\ s\ ([UNMARK,\ JUMP\ b,\ LABEL\ a]\ \#\ \\
& \quad comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators} \} \\
& unwind\ s\ (comp\ d\ h\ \#\ [LABEL\ b]\ \#\ ops) \\
= & \quad \{ \text{induction hypothesis} \} \\
& unwind\ s\ ([LABEL\ b]\ \#\ ops) \\
= & \quad \{ \text{unwinding operators} \} \\
& unwind\ s\ ops
\end{aligned}$$

The last four steps in the above proof require that  $isFresh\ \alpha\ s$  for  $\alpha = c, a, d, b$  respectively, all of which were verified in the previous section.  $\square$

**Lemma 5** (*isFresh is monotonic*).

$$If\ a \leq b \wedge isFresh\ a\ s\ \text{ then } isFresh\ b\ s$$

That is, if one address is at most another, and the first is fresh with respect to a stack, then the second is also fresh with respect to this stack.

*Proof.* By induction on  $s :: Stack$ .

Case:  $s = []$

$$\begin{aligned}
& isFresh\ b\ [] \\
\Leftrightarrow & \quad \{ \text{definition of } isFresh \} \\
& True
\end{aligned}$$

Case:  $s = VAL\ n : s'$

$$\begin{aligned}
& isFresh\ b\ (VAL\ n : s') \\
\Leftrightarrow & \quad \{ \text{definition of } isFresh \} \\
& isFresh\ b\ s' \\
\Leftarrow & \quad \{ \text{induction hypothesis} \} \\
& a \leq b \wedge isFresh\ a\ s' \\
\Leftrightarrow & \quad \{ \text{definition of } isFresh \} \\
& a \leq b \wedge isFresh\ a\ (VAL\ n : s')
\end{aligned}$$

Case:  $s = HAN\ c : s'$



$$\begin{aligned}
& isFresh\ b\ (HAN\ c : s') \\
\Leftrightarrow & \quad \{ \text{definition of } isFresh \} \\
& b > c \wedge isFresh\ b\ s' \\
\Leftarrow & \quad \{ \text{induction hypothesis} \} \\
& b > c \wedge a \leq b \wedge isFresh\ a\ s' \\
\Leftarrow & \quad \{ \text{logic} \} \\
& a > c \wedge a \leq b \wedge isFresh\ a\ s' \\
\Leftarrow & \quad \{ \text{commutativity of } \wedge \} \\
& a \leq b \wedge a > c \wedge isFresh\ a\ s' \\
\Leftarrow & \quad \{ \text{definition of } isFresh \} \\
& a \leq b \wedge isFresh\ a\ (HAN\ c : s')
\end{aligned}$$

□

**Lemma 6** (*compile is non-decreasing*).

$$snd\ (compile\ a\ e) \geq a$$

That is, the next address returned by the compiler will always be greater than or equal to the address supplied as an argument.

*Proof.* By induction on  $e :: Expr$ .

Case:  $e = Val\ n$

$$\begin{aligned}
& snd\ (compile\ a\ (Val\ n)) \\
= & \quad \{ \text{definition of } compile \} \\
& a
\end{aligned}$$

Case:  $e = Throw$

$$\begin{aligned}
& snd\ (compile\ a\ Throw) \\
= & \quad \{ \text{definition of } compile \} \\
& a
\end{aligned}$$

Case:  $e = Add\ x\ y$

Let

$$\begin{aligned}
(\_, b) &= compile\ a\ x \\
(\_, c) &= compile\ b\ y
\end{aligned}$$

in

$$\begin{aligned}
& snd\ (compile\ a\ (Add\ x\ y)) \\
= & \quad \{ \text{definition of } compile \} \\
& c \\
= & \quad \{ \text{definition of } c \} \\
& snd\ (compile\ b\ y) \\
\geq & \quad \{ \text{induction hypothesis} \}
\end{aligned}$$

$$\begin{aligned}
& b \\
= & \{ \text{definition of } b \} \\
& \text{snd} (\text{compile } a \ x) \\
\geq & \{ \text{induction hypothesis} \} \\
& a
\end{aligned}$$

Case:  $e = \text{Catch } x \ h$

Let

$$\begin{aligned}
b & = \text{fresh } a \\
c & = \text{fresh } b \\
(\_, d) & = \text{compile } c \ x \\
(\_, e) & = \text{compile } d \ h
\end{aligned}$$

in

$$\begin{aligned}
& \text{snd} (\text{compile } a \ (\text{Catch } x \ h)) \\
= & \{ \text{definition of } \text{compile} \} \\
& e \\
= & \{ \text{definition of } e \} \\
& \text{snd} (\text{compile } d \ h) \\
\geq & \{ \text{induction hypothesis} \} \\
& d \\
= & \{ \text{definition of } d \} \\
& \text{snd} (\text{compile } c \ x) \\
\geq & \{ \text{induction hypothesis} \} \\
& c \\
> & \{ \text{definition of } c \} \\
& b \\
> & \{ \text{definition of } b \} \\
& a
\end{aligned}$$

□

**Lemma 7 (jumping compiled code).**

$$\text{If } a < b \text{ then } \text{jump } a \ (\text{comp } b \ e \ \# \ \text{ops}) = \text{jump } a \ \text{ops}$$

That is, jumping to an address in compiled code followed by arbitrary additional code gives the same result as simply jumping in the additional code, provided that the jump address is less than the initial address for the compiler.

*Proof.* By induction on  $e :: \text{Expr}$ .

Case:  $e = \text{Val } n$

$$\begin{aligned}
& \text{jump } a \ (\text{comp } b \ (\text{Val } n) \ \# \ \text{ops}) \\
= & \{ \text{definition of } \text{comp} \} \\
& \text{jump } a \ ([\text{PUSH } n] \ \# \ \text{ops}) \\
= & \{ \text{definition of } \text{jump} \} \\
& \text{jump } a \ \text{ops}
\end{aligned}$$

Case:  $e = Throw$

$$\begin{aligned} & jump\ a\ (comp\ b\ Throw\ ++\ ops) \\ = & \quad \{ \text{definition of } comp \} \\ & jump\ a\ ([THROW]\ ++\ ops) \\ = & \quad \{ \text{definition of } jump \} \\ & jump\ a\ ops \end{aligned}$$

Case:  $e = Add\ x\ y$

Let

$$\begin{aligned} (\_ , c) &= compile\ b\ x \\ (\_ , \_ ) &= compile\ c\ y \end{aligned}$$

in

$$\begin{aligned} & jump\ a\ (comp\ b\ (Add\ x\ y)\ ++\ ops) \\ = & \quad \{ \text{definition of } comp \} \\ & jump\ a\ (comp\ b\ x\ ++\ comp\ c\ y\ ++\ [ADD]\ ++\ ops) \\ = & \quad \{ \text{induction hypothesis} \} \\ & jump\ a\ (comp\ c\ y\ ++\ [ADD]\ ++\ ops) \\ = & \quad \{ \text{induction hypothesis} \} \\ & jump\ a\ ([ADD]\ ++\ ops) \\ = & \quad \{ \text{definition of } jump \} \\ & jump\ a\ ops \end{aligned}$$

The first induction hypothesis step in the above proof requires that  $a < b$ , which is true by assumption, and the second that  $a < c$ , which is verified as follows:

$$\begin{aligned} & a \\ < & \quad \{ \text{assumption} \} \\ & b \\ \leq & \quad \{ compile\ \text{is non-decreasing (lemma 6)} \} \\ & snd\ (compile\ b\ x) \\ = & \quad \{ \text{definition of } c \} \\ & c \end{aligned}$$

Case:  $e = Catch\ x\ h$

Let

$$\begin{aligned} c &= fresh\ b \\ d &= fresh\ c \\ (\_ , e) &= compile\ d\ x \\ (\_ , \_ ) &= compile\ e\ h \end{aligned}$$

in

$$\begin{aligned}
& \text{jump } a \text{ (comp } b \text{ (Catch } x \text{ } h) \text{ ++ ops)} \\
= & \quad \{ \text{definition of comp} \} \\
& \text{jump } a \text{ ([MARK } b \text{] ++ comp } d \text{ } x \text{ ++ [UNMARK, JUMP } c, \\
& \quad \text{LABEL } b \text{] ++ comp } e \text{ } h \text{ ++ [LABEL } c \text{] ++ ops)} \\
= & \quad \{ \text{definition of jump} \} \\
& \text{jump } a \text{ (comp } d \text{ } x \text{ ++ [UNMARK, JUMP } c, \\
& \quad \text{LABEL } b \text{] ++ comp } e \text{ } h \text{ ++ [LABEL } c \text{] ++ ops)} \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{jump } a \text{ ([UNMARK, JUMP } c, \text{ LABEL } b \text{] ++} \\
& \quad \text{comp } e \text{ } h \text{ ++ [LABEL } c \text{] ++ ops)} \\
= & \quad \{ \text{definition of jump} \} \\
& \text{jump } a \text{ (comp } e \text{ } h \text{ ++ [LABEL } c \text{] ++ ops)} \\
= & \quad \{ \text{induction hypothesis} \} \\
& \text{jump } a \text{ ([LABEL } c \text{] ++ ops)} \\
= & \quad \{ \text{definition of jump} \} \\
& \text{jump } a \text{ ops}
\end{aligned}$$

The last four steps in the above proof require that  $a < d$ ,  $a \neq b$ ,  $a < e$  and  $a \neq c$  respectively, all of which are verified by the following calculation:

$$\begin{aligned}
& a \\
< & \quad \{ \text{assumption} \} \\
& b \\
< & \quad \{ \text{definition of } c \} \\
& c \\
< & \quad \{ \text{definition of } d \} \\
& d \\
\leq & \quad \{ \text{compile is non-decreasing (lemma 6)} \} \\
& \text{snd (compile } d \text{ } x) \\
= & \quad \{ \text{definition of } e \} \\
& e
\end{aligned}$$

□

## 9 Further work

We have shown how the compilation of exceptions using stack unwinding can be explained and verified in a simple manner. In this final section we briefly describe a number of possible directions for further work.

- *Mechanical verification.* The correctness of our two compilers for exceptions has also been verified mechanically. In particular, theorem 3 was verified in Lego by McBride [6], and theorem 4 in Isabelle by Nipkow [10]. A novel aspect of the Lego verification that merits further investigation is the use of dependent types to precisely capture the stack demands of the virtual machine operations (e.g. add requires a stack with two integers on the top),

which leads to a further simplification of our correctness proof, at the expense of requiring a more powerful type system.

- *Modular compilers.* Inspired by the success of using monads to define the denotational semantics of languages in terms of the semantics of individual features [9], similar techniques are now being applied to build compilers in a modular manner [4]. To date, however, this work has not considered the compilation of exceptions, so there is scope for trying to incorporate the present work into this modular framework.
- *Calculating the compiler.* Rather than first defining the compiler and virtual machine and then proving their correctness with respect to the semantics, another approach would be to try and calculate the definition of these functions starting from the compiler correctness theorem itself [7, 1], with the aim of giving a systematic *discovery* of the idea of compiling exceptions using stack unwinding, as opposed to a post-hoc verification.
- *Generalising the language.* Arithmetic expressions with exceptions served as a suitable language for the purposes of this article, but it is important to explore how our approach can be scaled to more expressive languages, such as a simple functional or imperative language, to languages with more than one kind of exception and user-defined exceptions, and to other notions of exception, such as imprecise [13] and asynchronous [5] exceptions.
- *Compiler optimisations.* The basic compilation method presented in this article can be optimised in a number of ways. For example, we might rearrange the compiled code to avoid the need to jump over handlers in the case of no exceptions being thrown, use a separate stack of handler addresses to make the process of stack unwinding more efficient, or use a separate table of handler scopes to avoid an execution-time cost for installing a handler. It would be interesting to consider how such optimisations can be incorporated into our compiler and its correctness proof.

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QuickCheck [2] was used extensively in the production of this article, and proved invaluable as an aid to getting the definitions and results correct before proceeding to formal proofs. A number of (often subtle) mistakes in our definitions and results were discovered in this way.

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